

FIGURE 1

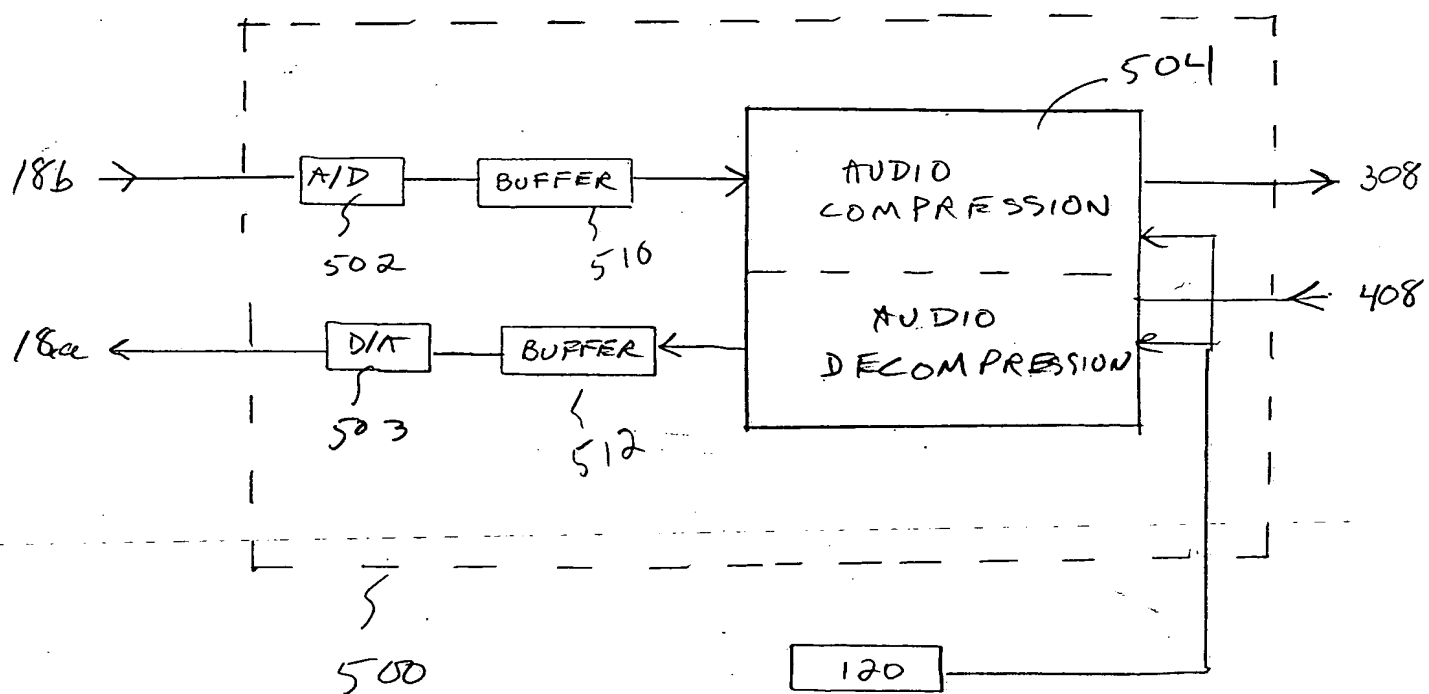


FIGURE 3C

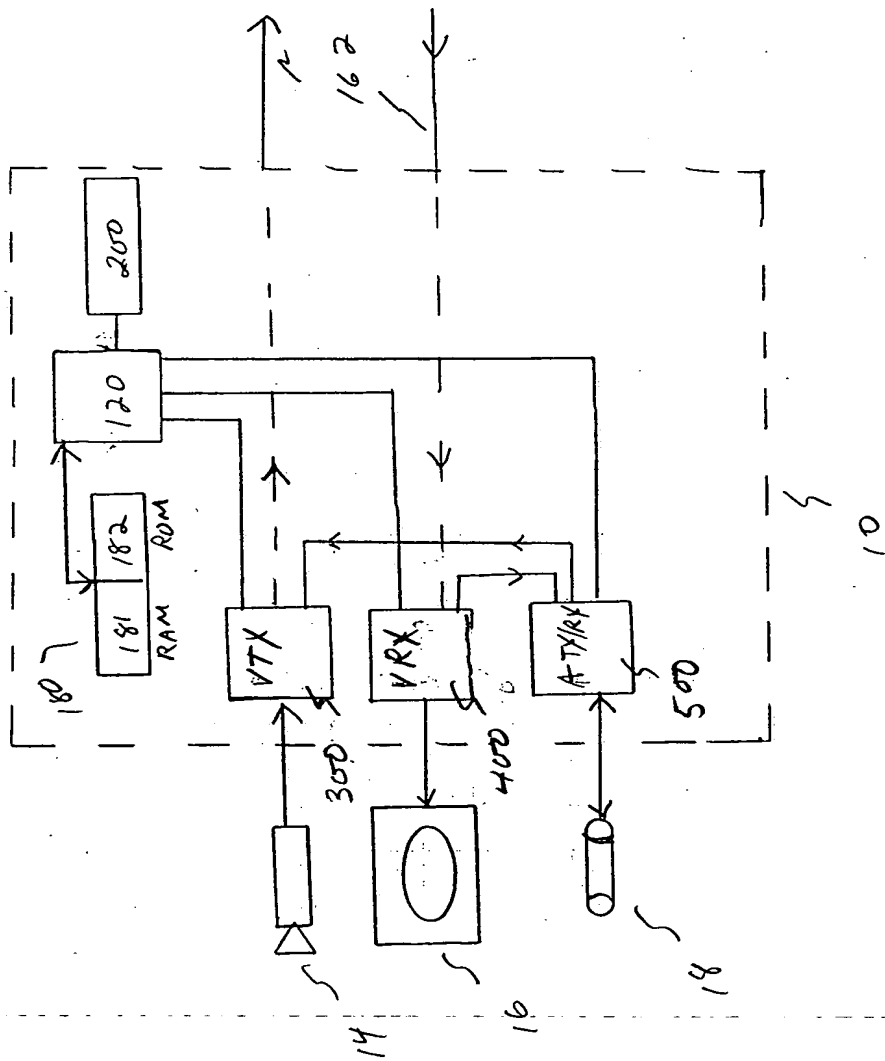


FIGURE 2

Video Codec TX Board

304

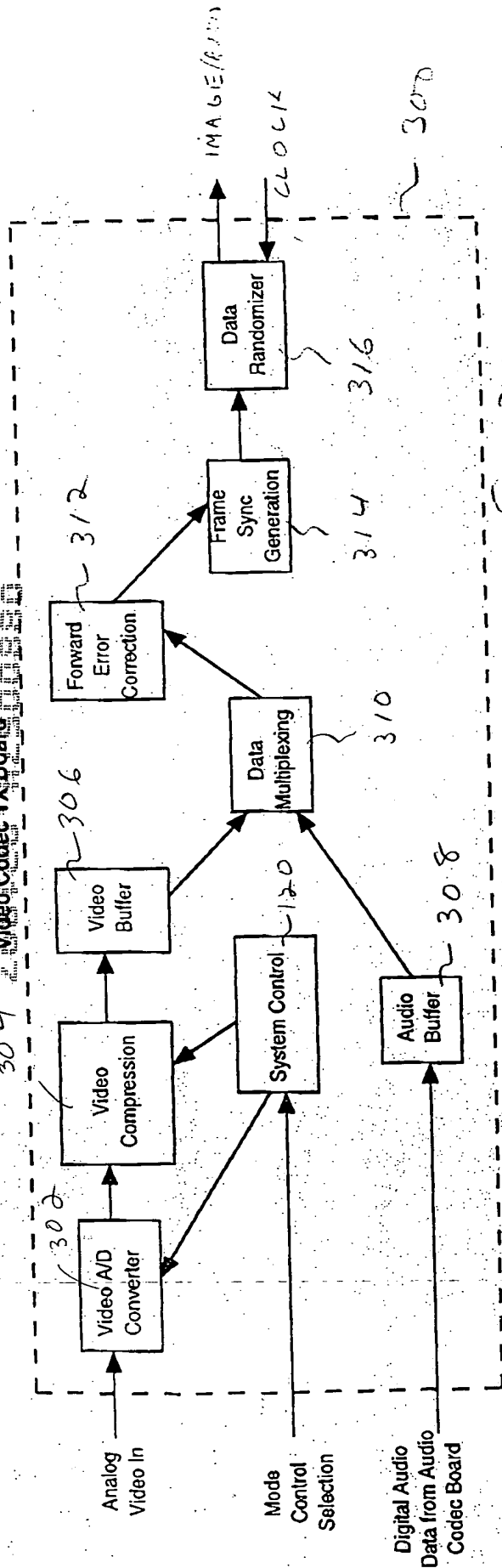


FIG. 3A

Video Codec RX Board

400

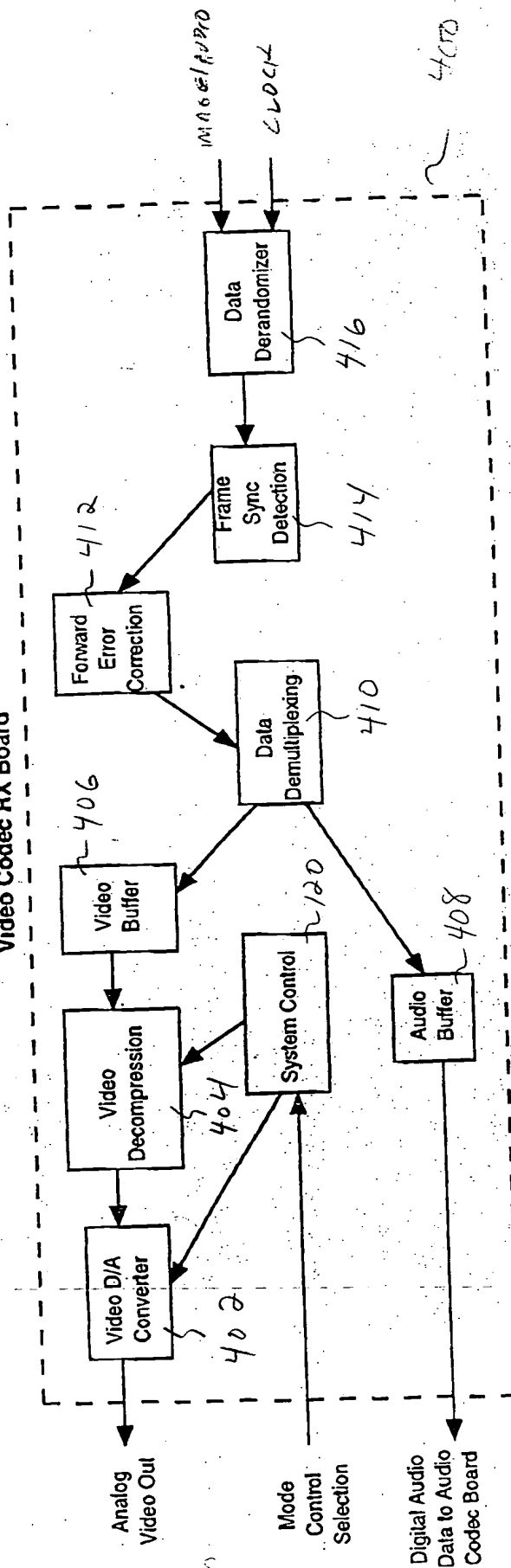


FIG. 3B

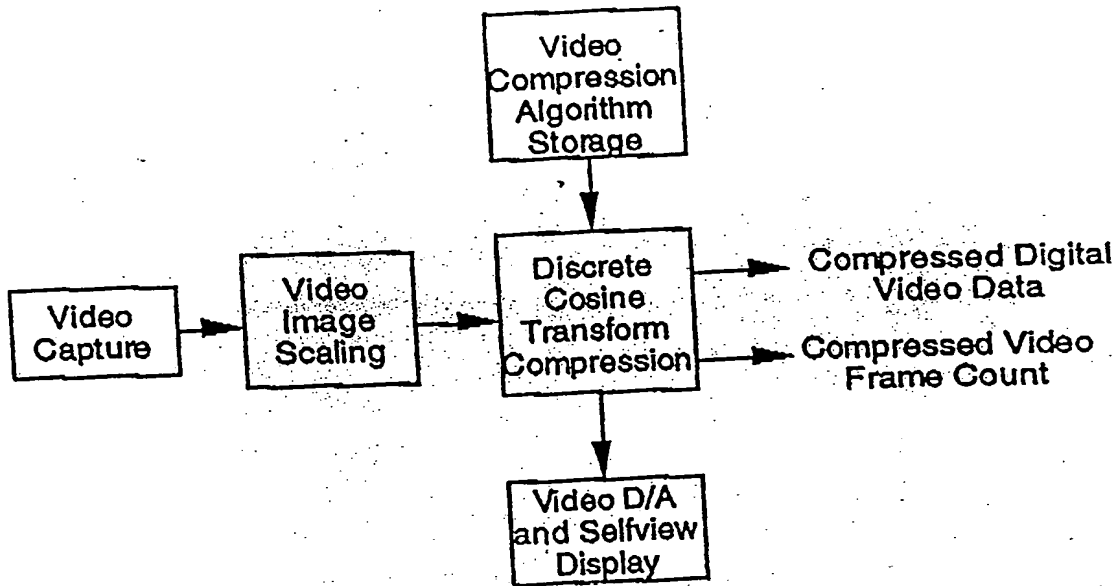


FIG. 4A

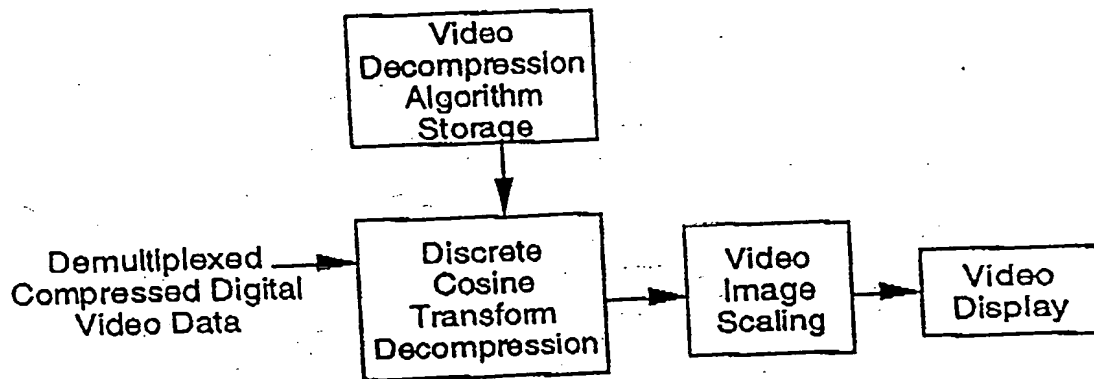


FIG 4B

2000-4-500000

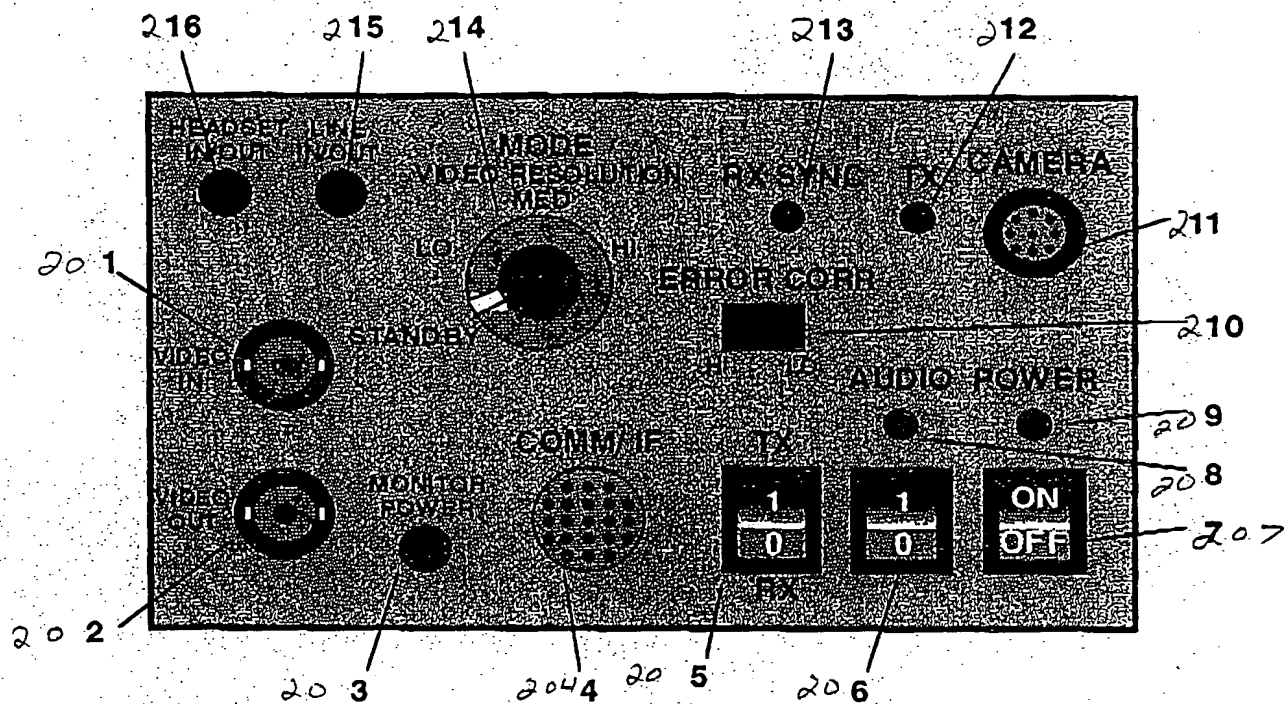


FIGURE 5a

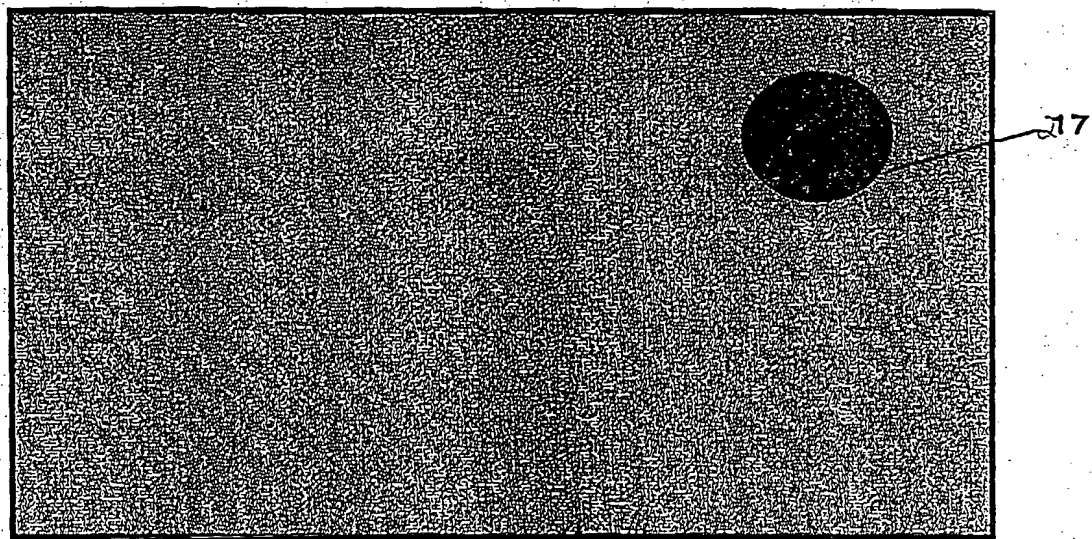


FIGURE 5b

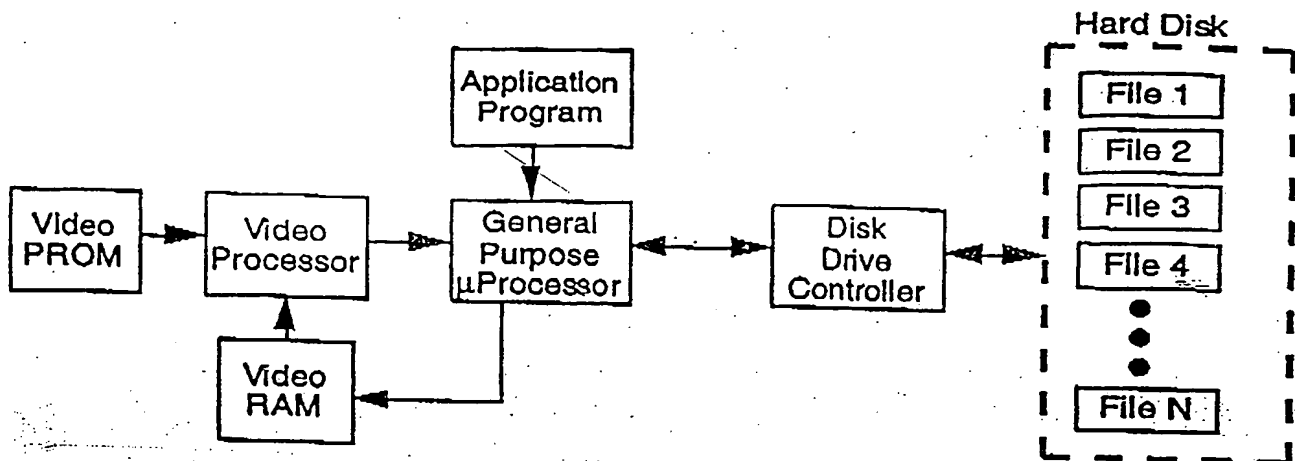


FIG. 6A

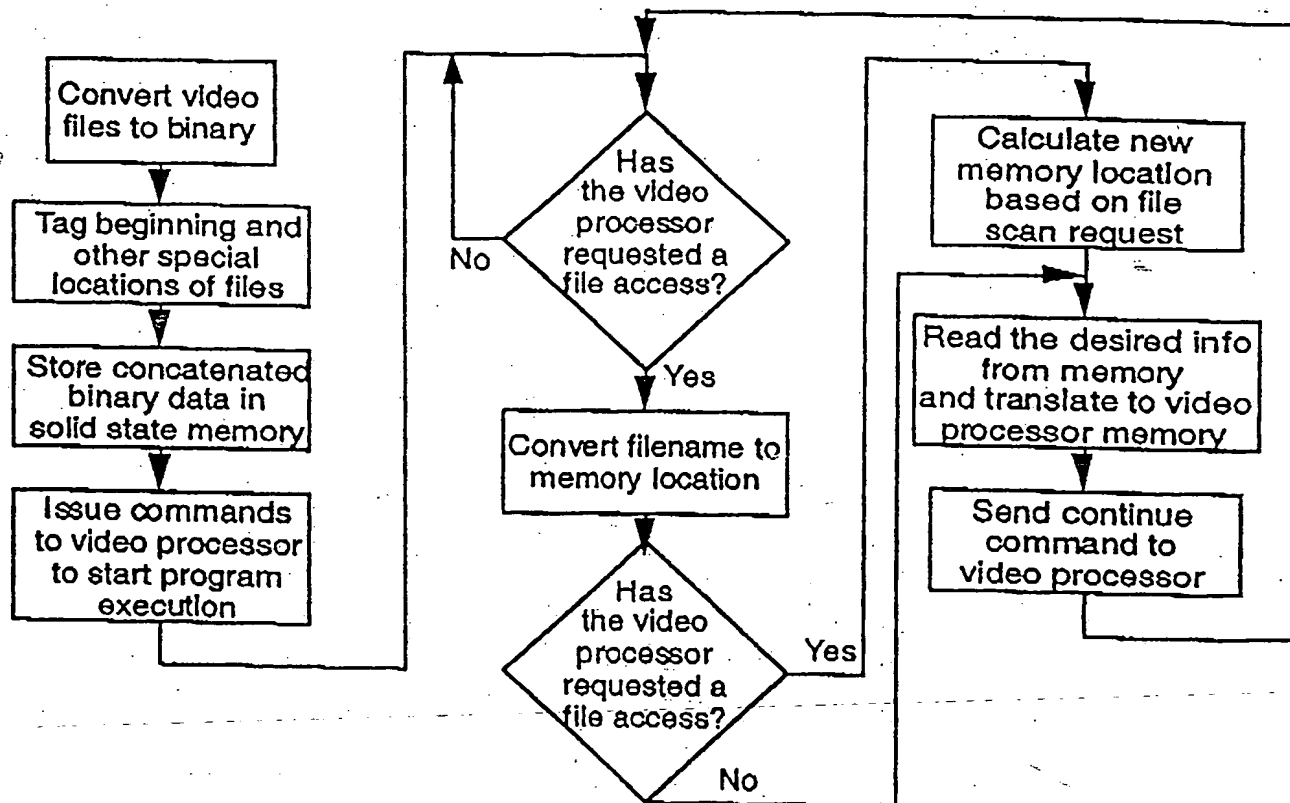


FIG. 6B

Clock Rate - 16 kHz or 32 kHz

Frame Size - 200 bytes (LO Error Correction Mode)

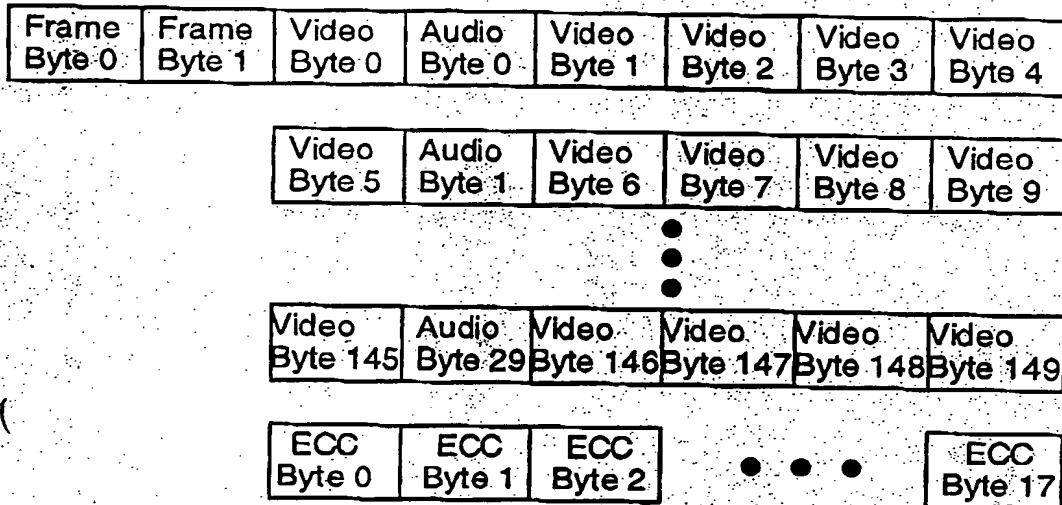


FIGURE 7a

Clock Rate - 64 kHz or 128 kHz

Frame Size - 200 bytes (LO Error Correction Mode)

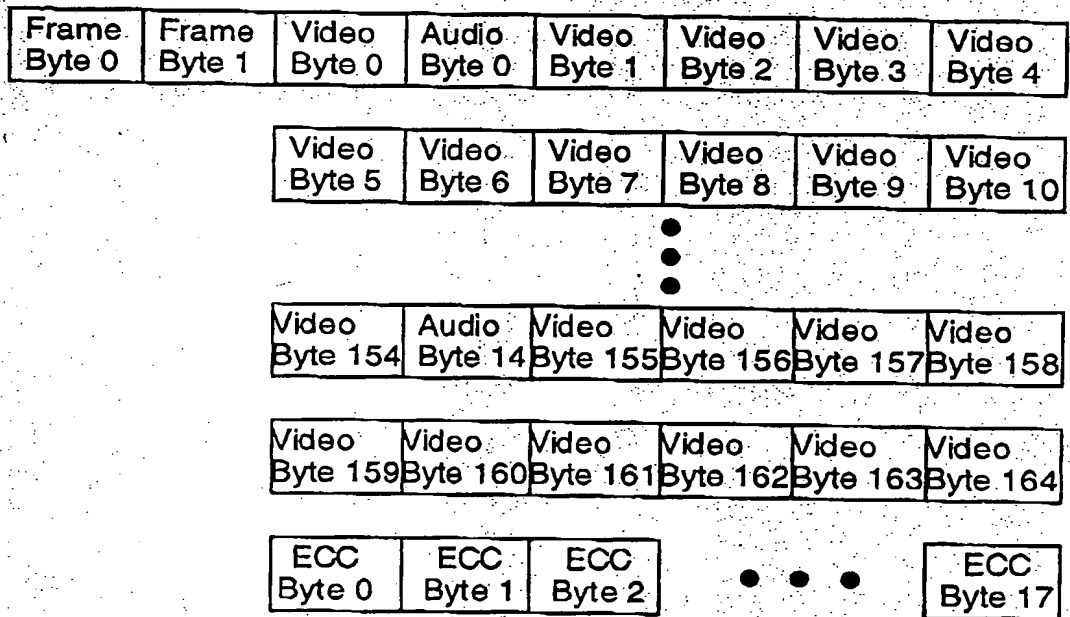


FIGURE 7b

Clock Rate - 16 kHz or 32 kHz
Frame Size bytes (HI Error Correction Mode)

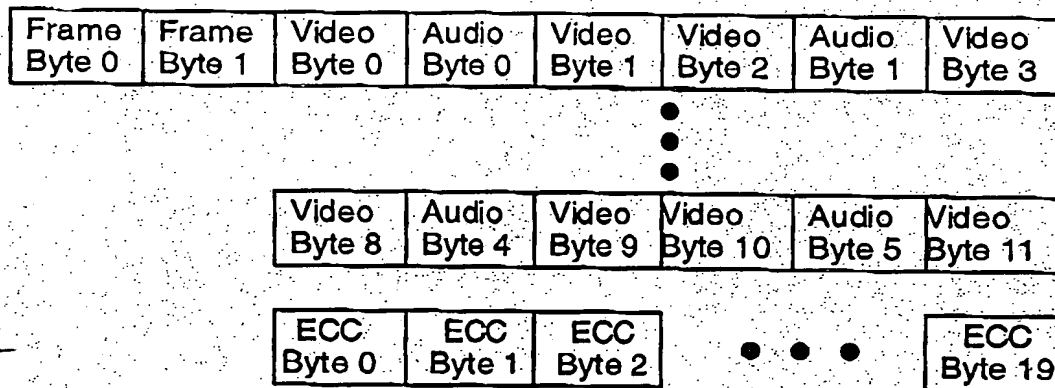


FIGURE 7c

Clock Rate - 64 kHz or 128 kHz
Frame Size bytes (HI Error Correction Mode)

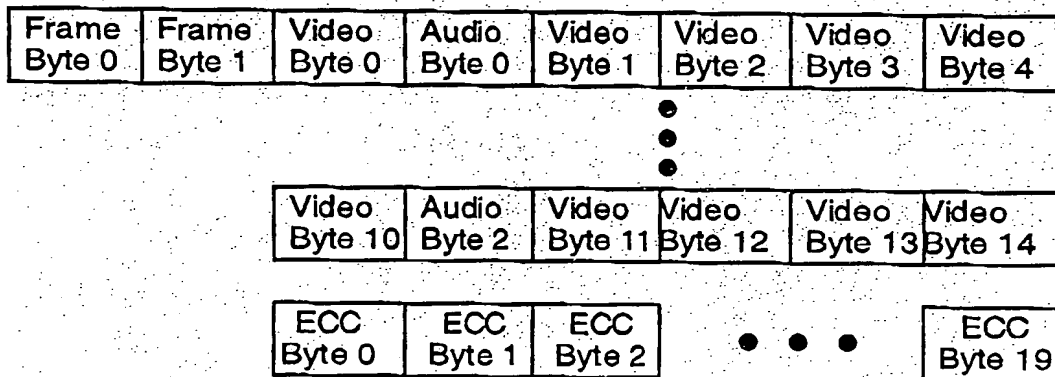
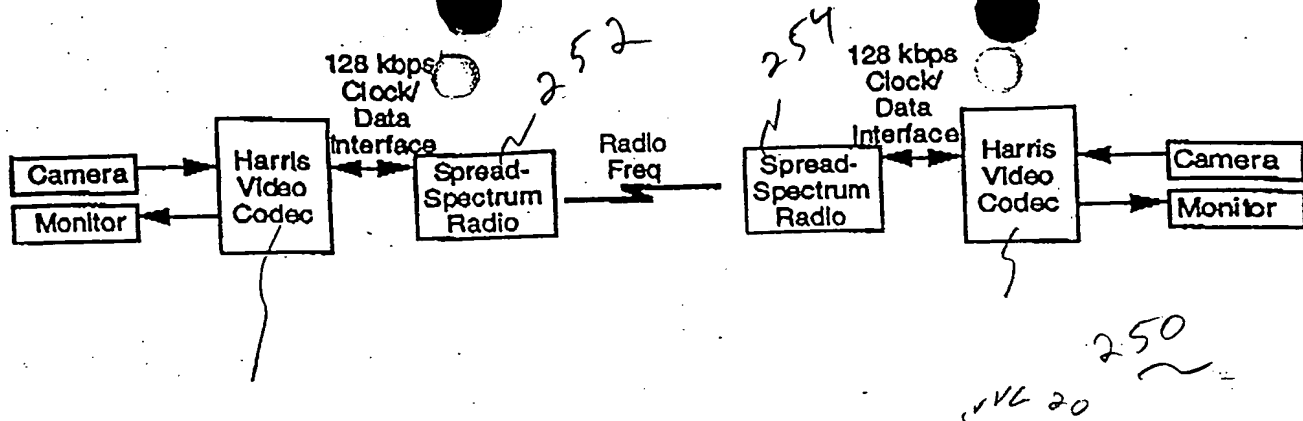


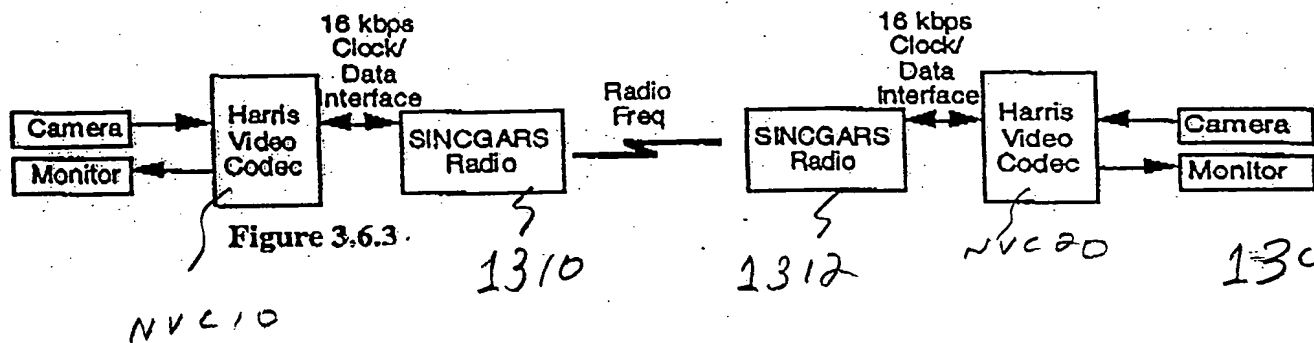
Figure 9.0-1 - Video/Audio Frame Structure

FIGURE 7d



NVC 10

FIG. 8



NVC 10

FIG. 9

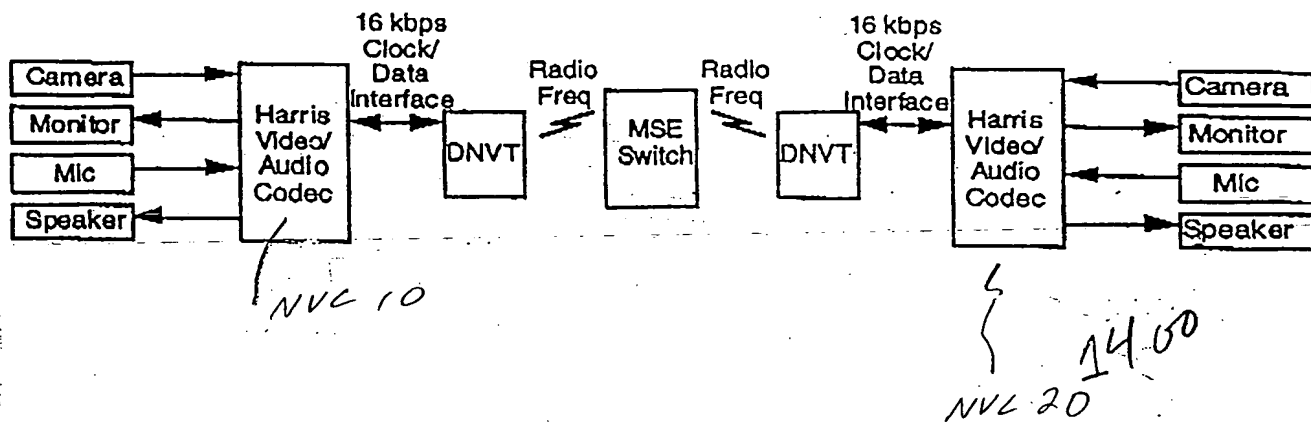


FIG. 10

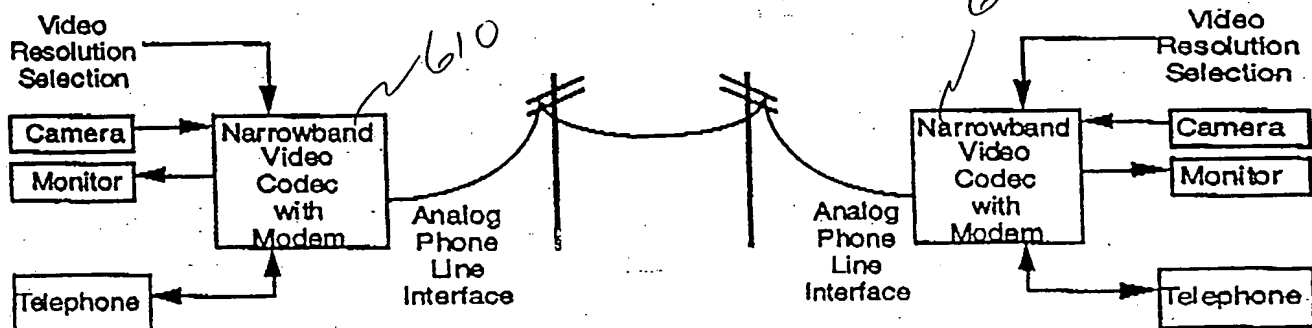


Figure 3.6.5

Fig 11

600

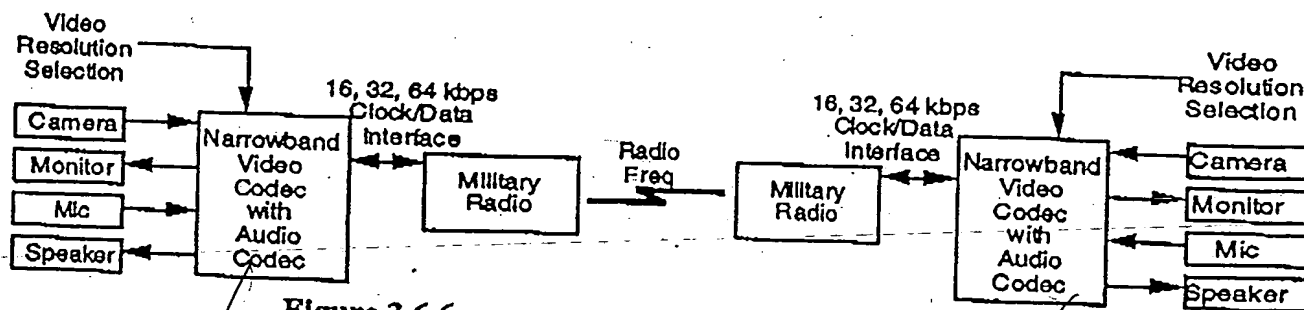


Figure 3.6.6

12

Fig

712